## **PATENT APPLICATION**

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re A	pplication of:	)	•
	••	:	Examiner: David J. Huisman
ATSUSHI DATE		)	O A.4 II.'4 0192
Applic	eation No.: 10/671,785	; )	Group Art Unit: 2183
Filed:	September 29, 2003	) :	
For:	PROCESSOR SYSTEM WITH EXCLUSIVE ACCESS TO ON-CHIP MEMORY BY ONE OF A BUILT-IN PROCESSOR OR AN EXTERNAL PROCESSOR	; ; ;	(Monday) January 26, 2009
Comm P.O. B	top RCE dissioner for Patents ox 1450		

## **PRELIMINARY AMENDMENT**

Sir:

In response to the Office Action dated August 25, 2009, the period for response to which having been extended to Monday, January 26, 2009 by the accompanying Petition for Extension of Time and in connection with which a Request for Continued Examination (RCE) is being filed concurrently herewith, please amend the above-identified application, as follows:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

January 26, 2009	
(Date of Deposit)	
Michael K. O'Neill (Reg. No. 32,622)	
(Name of Attorney for Applicant)	

Western Kontine

Date of Signature